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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/632,772	08/01/2003	Gregg Bernard Lesartr	200209214-1	3656	
22879 HEWLETT PA	7590 11/30/2007 CKARD COMPANY	EXAMINER			
P O BOX 272400, 3404 E. HARMONY ROAD			WAI, ERIC CHARLES		
INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400		NISTRATION	ART UNIT	PAPER NUMBER	
			2195		
			MAIL DATE	DELIVERY MODE	
			11/30/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	•	
10/632,772	LESARTR ET AL.		
Examiner	Art Unit		
Eric C. Wai	2195		

	`	Eric C. Wai	2195	
	The MAILING DATE of this communication appe	ars on the cover sheet with the	correspondence add	ress
THE REF	PLY FILED 09 November 2007 FAILS TO PLACE THIS	S APPLICATION IN CONDITION F	OR ALLOWANCE.	
1. ⊠ The this plad a R	reply was filed after a final rejection, but prior to or or application, applicant must timely file one of the followices the application in condition for allowance; (2) a Not equest for Continued Examination (RCE) in compliance periods:	the same day as filing a Notice of wing replies: (1) an amendment, a stice of Appeal (with appeal fee) in	f Appeal. To avoid aba ffidavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)
	The period for reply expires <u>3</u> months from the mailing date. The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire I Examiner Note: If box 1 is checked, check either box (a) or TWO MONTHS OF THE FINAL REJECTION. See MPEP 7	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailin (b). ONLY CHECK BOX (b) WHEN TH	ng date of the final rejecti	on.
have been under 37 C set forth in may reduc	s of time may be obtained under 37 CFR 1.136(a). The date filed is the date for purposes of determining the period of exCFR 1.17(a) is calculated from: (1) the expiration date of the solution (b) above, if checked. Any reply received by the Office laters any earned patent term adjustment. See 37 CFR 1.704(b) OF APPEAL	tension and the corresponding amoun shortened statutory period for reply ori r than three months after the mailing d	t of the fee.  The appropr ginally set in the final Offi	iate extension fee ce action; or (2) a
filin	Notice of Appeal was filed on A brief in comp g the Notice of Appeal (37 CFR 41.37(a)), or any exte otice of Appeal has been filed, any reply must be filed MENTS	nsion thereof (37 CFR 41.37(e)), t	o avoid dismissal of th	ns of the date of e appeal. Since
3.	e proposed amendment(s) filed after a final rejection.  They raise new issues that would require further co  They raise the issue of new matter (see NOTE belo  They are not deemed to place the application in be appeal; and/or  They present additional claims without canceling a	nsideration and/or search (see NC w); tter form for appeal by materially r	OTE below); educing or simplifying	
5. 🗌 Ap 6. 📗 Ne	NOTE: (See 37 CFR 1.116 and 41.33(a)). e amendments are not in compliance with 37 CFR 1.1 plicant's reply has overcome the following rejection(s) wly proposed or amended claim(s) would be a	21. See attached Notice of Non-C		
7. X For how The Cla Cla Cla Cla	i-allowable claim(s). In purposes of appeal, the proposed amendment(s): a), with the new or amended claims would be rejected is prosestatus of the claim(s) is (or will be) as follows: im(s) allowed: None.  im(s) objected to: None.  im(s) rejected: 1,3-8,12-14,16,17 and 21-31.  im(s) withdrawn from consideration: None.		vill be entered and an e	explanation of
bec was	e affidavit or other evidence filed after a final action, busause applicant failed to provide a showing of good and not earlier presented. See 37 CFR 1.116(e).	d sufficient reasons why the affida	vit or other evidence is	s necessary and
ente sho	e affidavit or other evidence filed after the date of filing ered because the affidavit or other evidence failed to o wing a good and sufficient reasons why it is necessar he affidavit or other evidence is entered. An explanatio	overcome <u>all</u> rejections under appey y and was not earlier presented.	eal and/or appellant fa See 37 CFR 41.33(d)(	ils to provide a 1).
	ie anidavit of other evidence is entered. An explanation IT FOR RECONSIDERATION/OTHER	n of the status of the claims after t	entry is below or attach	iea.
	ne request for reconsideration has been considered bu	it does NOT place the application	in condition for allowa	nce because:
	te the attached Information Disclosure Statement(s). her: See Continuation Sheet.	(PTO/SB/08) Paper No(s)	4 4	
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Continuation of 13. Other:

- 1. Applicant's arguments filed 11/09/2007 have been fully considered but they are not persuasive.
- Applicant argues:

Indeed, it appears that the memory queue 36 is checked for coherency regardless of whether any of the translation pairs in the TLB correspond to the alleged "purge signal" (see Figure 5, blocks 122 and 124), and there is nothing in Moore to suggest that the searching of the memory queue 36 should be affected in any way based on a determination as to whether any of the translation pairs in the TLB correspond to the "purge signal." Accordingly, the Office Action fails to establish a prima facie case of obviousness with respect to at least the features of "the logic further configured to transmit, based on the determination, a purge detection signal indicative of whether at least one translation pair in the TLB corresponds to the purge signal and to determine, based upon the purge detection signal, whether to search the memory cache for information to be purged based on the purge signal," as recited by claim 1.

3. Examiner disagrees. AAPA in [0005] lines 5-8 recites, "so that if a processor's TLB contains a translation pair related to the modifications in the page table, then the processor can purge the TLB translation pair and purge any mini-TLBs or instruction queues that may attempt to use data related to the deleted address." (Emphasis added). In the cited text, the determination to purge instruction queues is performed based on the processor's TLB containing a translation pair related to the modification in the page table. While Examiner agrees that Moore teaches checking the memory queues for coherency regardless on any corresponding translation pairs in the

TLB, AAPA is clear that doing so based on a "purge detection signal" is well known in the prior art.